IN THE ABSTRACT OF THE DISCLOSURE

Amend the Abstract as follows:

In microcomputers and digital signal processors in ~~processorspin~~ which a central processing unit for controlling the entire system and a digital signal processing unit having a product sum function required to process digital signals efficiently are mounted on one and the same chip ~~chippthis~~, ~~invention prevents~~ an increase in the number of processing steps caused by differing ~~differeing~~ types of data handled by the calculators is prevented, thereby enhancing the efficiency of the digital signal processing.

~~The digital signal processing unit is made a calculation unit that handles fixed-point data, and an instruction calling for exeuction of a fixed-point data calculation is provided separately from the conventional integer calculation instruction. When, in the data transfer between the digital signal processing unit and memories or external circuits, data shorter in bit length than the calculation precision is transferred, the calculation unit has a function to input and output data to and from the higher-order side of the register in which the data is stored and the fixed-point data transfer~~

~~instruction is provided separately from the conventional integer data transfer instruction.~~

~~This invention can eliminate additional correction processing necessitated when the integer data processing unit is made to exeucte the digitalsignal processing.~~